

## BCA-122 Logical Organization of Computers – II

**Maximum Marks: 100**  
**Minimum Pass Marks: 35**  
**Time: 3 hours**

**External: 80**  
**Internal: 20**

**Note:** Examiner will be required to set Nine Questions in all. First Question will be compulsory, consisting of objective type/short-answer type questions covering the entire syllabus. In addition to that eight more questions will be set, two questions from each Unit. Student will be required to attempt FIVE questions in all. Question Number 1 will be compulsory. In addition to compulsory question, student will have to attempt four more questions selecting one question from each Unit.

### UNIT - I

Sequential Logic: Characteristics, Flip-Flops, Clocked RS, D type, JK, T type and Master-Slave flip-flops. State table, state diagram. Flip-flop excitation tables

### UNIT - II

Sequential Circuits: Designing registers – Serial Input Serial Output (SISO), Serial Input Parallel Output (SIPO), Parallel Input Serial Output (PISO), Parallel Input Parallel Output (PIPO) and shift registers. Designing counters – Asynchronous and Synchronous Binary Counters, Modulo-N Counters and Up-Down Counters

### UNIT - III

Memory & I/O Devices: Memory Parameters, Semiconductor RAM, ROM, Magnetic and Optical Storage devices, Flash memory, I/O Devices and their controllers.

### UNIT - IV

Instruction Design & I/O Organization: Machine instruction, Instruction set selection, Instruction cycle, Instruction Format and Addressing Modes. I/O Interface, Interrupt structure, Program-controlled, Interrupt-controlled & DMA transfer, I/O Channels, IOP.

### TEXT BOOKS

1. M. Morris Mano, Digital Logic and Computer Design, Prentice Hall of India Pvt. Ltd.
2. V. Rajaraman, T. Radhakrishnan, An Introduction to Digital Computer Design, Prentice Hall of India Pvt. Ltd.

### REFERENCE BOOKS

1. Andrew S. Tanenbaum, Structured Computer Organization, Prentice Hall of India Pvt. Ltd.
2. Nicholas Carter, Schaum's Outlines Computer Architecture, Tata McGraw-Hill