Semester 1 & 2

Course: B.Sc. (Hons) IT

Paper Code: BSIT-207 (IT LAB-I) Nomenclature: Analog Electronics (Practical)

M.M: 50 Time: 3 hrs

Instruction for the examiners: A candidate is required to perform one experiment out of the list below.

Note:-

- 1. This practical paper is to be studied during Semester 1 & 2. But the examination is to be held after Semester 2 only.
- 2. A candidate is required to perform 6 Experiments from the list given below.

3. The number of students in a group must not exceed 10.

4. Workload – 3 periods per week per group

Distribution Of Marks

Experiment: 25 Practical Work Book: 15

Viva

Voce: 10

- 1. To study the V-I characteristics of PN junction diode.
- 2. To study the zener diode as voltage regulator.
- 3. To study half wave voltage multiplier Ckt. Using diode.
- 4. To study HWR and FWR and measurement of ripple factor with and with C filter.
- 5. To study diode as shunt clipping clement.
- 6. To study diode as clamping element.
- 7. Study of CB characteristics and calculate H parameter from graph.
- 8. Study of CE characteristics and calculate H parameter from graph.
- 9. Study of JFET characteristics.
- 10. To measure Av. Ai. Ap. In CB. CC Transistor amplifier.
- 11. Study of fixed bias arrangement for transistor.