

**Semester 1 & 2**  
**Course: B.Sc. (Hons) IT**  
**Paper Code: BSIT-208 (IT LAB-II)**  
**Nomenclature: Digital Electronics (Practical)**

**M.M: 50**  
**Time: 3 hrs**

**Instruction for the examiners:** A candidate is required to perform one experiment out of the list below.

**Note:-**

1. This practical paper is to be studied during Semester 1 & 2. But the examination is to be held after Semester 2 only.
2. A candidate is required to perform 6 Experiments from the list given below.
3. The number of students in a group must not exceed 10.
4. Workload – 3 periods per week per group

**Distribution of Marks**

Experiment: 25  
Practical Work Book: 15  
Viva Voce: 10

**LIST OF EXPERIMENTS**

1. Measurement of voltage. Time period and phase-shift Biasing CRO.
2. Measurement of resistance by colour code method and using M/M and to design a potential divider arrangement and familiarization of components such as capacitors, potentiometer, diode, transistors, etc.
3. Study of basis logic gate (AOI).
4. Study of DTL NAND gate.
5. Study of TTL NANA gate.
6. Digital trainer using AOI.
7. Digital trainer using NAND.
8. To study of RC Ckts. As integrating and differentiating Ckts.
9. To verify maximum power transfer theorem for DC network
10. To study RC low pass filter and measurement of cut-off frequency from graph.
11. To study RC High pass Filter.
12. To study the application of Superposition theorem.