

**KURUKSHETRA UNIVERSITY KURUKSHETRA**  
**Course: B.Sc. (Hons) IT**  
**Semester-IV**

**Paper Code: BSIT-408**  
**Nomenclature of Paper - Digital Electronics (I.T Lab-VI)**

**Note:-**

1. A student is required to perform a minimum of 7 experiments from the list given below during course of study(Semester III +Semester IV).
2. Instructions for Examiners: A candidate is required to perform one experiment.
3. Maximum number of students should not exceed ten in one group during course of studies as well as annual examination.

**M.M: 50**

EXPERIMENT (3 hrs)	: 25
PRACTICAL WORK BOOK	: 15
VIVA-VOCE	: 10

**Time: 3 hrs**

**List of Experiments:-**

1. Half adder/Full adder.
2. Multiplexer and Demultiplexer Circuit (4: 1)
3. JK, D & T Flip-Flops.
4. Divide by N Counter.
5. Shift register
6. DAC
7. Ripple' binary counter.
8. Synchronous binary-counter.
9. Up. Down counter
10. Code Converter.
11. Parity generator
12. C. MOS Decade Counter