

Semester-V
Course: B.Sc (Hons.)IT
Paper Code: BSIT 505
Nomenclature: - Microprocessor Architecture and Programming-III

Max. Marks: 45+5*
Time: 3hrs.

UNIT-I

Introduction to stack, stack structure of 8086, interrupts and interrupts service routines, interrupt cycle of 8086, non maskable interrupt, maskable interrupt (INTR), interrupt programming, passing parameters to procedures, handling programs of size more than 64K, MACROS, timings and delays.

UNIT-II

Basic Peripherals and their Interfacing with 8086: semiconductor memory interfacing, dynamic RAM interfacing, interfacing I/O ports.

UNIT-III

Interfacing A/D data converters, interfacing D/A converters, Interconnection topologies, software aspects of multimicroprocessor systems, Numeric Processor 8087,

UNIT-IV

Salient features 80286, 80386, 80586, Pentium 4, Basic features of RISC Processors and Design issues of RISC Processors.

References:

1. Advanced Microprocessors & Peripherals By Ray & Bhurchandi (Tata McGraw Hill)
2. Microprocessors and Interfacing (Programming and Hardware) by Douglas V. Hall.
3. Advance Microprocessors & IBM-PC Assembly language Programs By Udaya Kumar
4. Microprocessors Principles and Application 2nd edition By Gilmore