

**Semester-VI**  
**Course. B.Sc. (Hons) IT**  
**Paper Code: BSIT - 601**  
**Nomenclature: - Computer System Architecture-II**

**Max. Marks: 45+5\***  
**Time: 3hrs.**

**Unit –I**

**Pipeline and Vector processing:** Parallel Processing, pipelining, Instruction Pipeline, Risc Pipeline , Vector Processing .

**Unit-II**

**Memory Organization:** Memory hierarchy, Auxiliary Memory, Associative Memory, Interleaved memory, Cache memory, Virtual Memory, Memory Management Hardware.

**Unit-III**

**Multiprocessors :** Characteristics of Multiprocessors, Interconnection Structures.

**Unit-IV**

**Input Output Organization :** Peripheral devices , Inut-Output Interface, Asynchronous data transfer, Modes of Transfer, Priority Interrupt, Direct Memory Access(DMA),Input-Output Processor(IOP),Serial Communication

**References:**

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|---|----------------|
| 1. Computer System Architecture           | By. Moris Mano |
| 2. Computer Architecture and Organization | By J.P. Hayes  |