

PAPER-II Logical Organization of Computers

Maximum Marks: 50
Minimum Pass Marks:
Time: 3 hours

External: 40
Internal: 10

Note: Examiner will be required to set Nine Questions in all. First Question will be compulsory, consisting of objective type/short-answer type questions covering the entire syllabus. In addition to that eight more questions will be set, two questions from each Unit. Student will be required to attempt FIVE questions in all. Question Number 1 will be compulsory. In addition to compulsory question, student will have to attempt four more questions selecting one question from each Unit.

UNIT - I

Information Representation: Number Systems, Binary Arithmetic, Fixed -point and Floating- point representation of numbers, BCD Codes, Error detecting and correcting codes, Character Representation – ASCII, EBCDIC.

UNIT - II

Binary Logic: Boolean Algebra, Boolean Theorems, Boolean Functions and Truth Tables, Canonical and Standard forms of Boolean functions, Simplification of Boolean Functions – Venn Diagram, Karnaugh Maps.

UNIT - III

Digital Logic: Basic Gates – AND, OR, NOT, Universal Gates – NAND, NOR, Other Gates – XOR, XNOR etc. Combinational Circuits: Half-Adder, Full-Adder, Half- Subtractor, Full-Subtractor, Encoders, Decoders, Multiplexers, Demultiplexers, Comparators, Code Converters.

UNIT IV

Sequential Logic: Characteristics, Flip-Flops, Clocked RS, D type, JK, T type and Master- Slave flip-flops. State table, state diagram. Flip-flop excitation tables
Shift registers : serial in parallel out and parallel in parallel out.. Designing counters – Asynchronous and Synchronous Binary Counters, Modulo-N Counters and Up-Down Counters

TEXT BOOKS

1. M. Morris Mano, Digital Logic and Computer Design, Prentice Hall of India Pvt. Ltd.
2. V. Rajaraman, T. Radhakrishnan, An Introduction to Digital Computer Design, Prentice Hall of India Pvt. Ltd.

REFERENCE BOOKS

