

LESSON PLAN FOR EVEN SEM
SESSION 2017-18

NAME OF ASSISTANT /ASSOCIATE PROFESSOR: GURMEET SINGH

CLASS/SECTION: B.Sc (H) I.T-IIInd SEM

**SUBJECT : EC-II(BSIT-205), AE PRACTICAL(BSIT-207),
DE PRACTICAL(BSIT-208),EC PRACTICAL(BSIT-209)**

UNIT/PART I	TOPIC	
	THEORY(BSIT-205)	PRACTICAL(BSIT-207,208 & 209)
DAY1 DATE 1-1-18	ASK	GROUP-B PAM
DAY2 DATE 2-1-18	FSK	GROUP-A PN JN DIODE GROUP-B PN JN DIODE
DAY3 DATE 3-1-18	PSK	GROUP-A PAM GROUP-B HA/FA
DAY4 DATE 8-1-18	PSK	GROUP-B PAM
DAY5 DATE 9-1-18	BPSK	GROUP-A PN JN DIODE GROUP-B PN JN DIODE
DAY6 DATE 10-1-18	BPSK	GROUP-A PAM GROUP-B HA/FA
DAY7 DATE 15-1-18	QPSK	GROUP-B PWM
DAY8 DATE 16-1-18	DPSK	GROUP-A HWR & FWR GROUP-B HWR & FWR
DAY9 DATE 17-1-18	SIMILARITIES BETWEEN BPSK & BFSK ASSIGNMENT 1	GROUP-A PWM GROUP-B MUX/DEMUX
DAY10 DATE 22-1-18	HOLIDAY	
DAY11 DATE 23-1-18	SPORTS DAY	
DAY12 DATE 24-1-18	HOLIDAY	
UNIT/PART II	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 29-1-18	EXAMPLES OF ERROR CONTROL CODING	GROUP-B PWM
DAY2 DATE 30-1-18	METHODS OF CONTROLLING ERRORS	GROUP-A HWR & FWR GROUP-B HWR & FWR
DAY3 DATE 31-1-18	HOLIDAY	
DAY4 DATE 5-2-18	TYPES OF ERRORS	GROUP-B PPM
DAY5	TYPES CODES	GROUP-A BASIC LOGIC GATES

DATE 6-2-18		GROUP-B BASIC LOGIC GATES
DAY6 DATE 7-2-18	MATRIX DESCRIPTION OF LINEAR BLOCK CODES	GROUP-A PWM GROUP-B MUX/DEMUX
DAY7 DATE 12-2-18	MATRIX DESCRIPTION OF LINEAR BLOCK CODES	GROUP-B PPM
DAY8 DATE 13-2-18	HOLIDAY	
DAY9 DATE 14-2-18	ERROR DETECTION AND ERROR CORRECTION CAPABILITIES OF LINEAR BLOCK CODES	GROUP-A PPM GROUP-B FLIP FLOPS
DAY10 DATE 19-2-18	ERROR DETECTION AND ERROR CORRECTION CAPABILITIES OF LINEAR BLOCK CODES CONTINUE	GROUP-B AMV
DAY11 DATE 20-2-18	ERROR DETECTION AND ERROR CORRECTION CAPABILITIES OF LINEAR BLOCK CODES	GROUP-A BASIC LOGIC GATES GROUP-B BASIC LOGIC GATES
DAY12 DATE 21-2-18	SINGLE ERROR CORRECTING HAMMING CODES	GROUP-A AMV GROUP-B FLIP FLOPS
UNIT/PART III	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 26-2-18	ALGEBARIC STRUCTURE OF CYCLIC CODES ASSIGNMENT 2	GROUP-B AMV
DAY2 DATE 27-2-18	ALGEBARIC STRUCTURE OF CYCLIC CODES	GROUP-A DTL/TTL GROUP-B DTL/TTL
DAY3 DATE 28-2-18	HOLIDAY	
DAY4 DATE 5-3-18	ENCODING USING AN (N-K) BIT SHIFT REGISTER	GROUP-B MMV
DAY5 DATE 6-3-18	ENCODING USING AN (N-K) BIT SHIFT REGISTER	GROUP-A DTL/TTL GROUP-B DTL/TTL
DAY6 DATE 7-3-18	ENCODING USING AN (N-K) BIT SHIFT REGISTER	GROUP-A MMV GROUP-B FLIP FLOPS
DAY 7 DATE 12-3-18	ENCODING USING AN (N-K) BIT SHIFT REGISTER	GROUP-B MMV
DAY8 DATE 13-3-18	SYNDROME CALCULATION	GROUP-A CE GROUP-B CE
DAY9 DATE 14-3-18	ERROR DETECTION AND ERROR CORRECTION	GROUP-A OF GROUP-B RBC
DAY10 DATE 19-3-18	ERROR DETECTION AND ERROR CORRECTION	GROUP-B OF
DAY11 DATE 20-3-18	CONDITIONAL TEST	GROUP-A CE GROUP-B CE
DAY12 DATE 21-3-18	CONDITIONAL TEST	GROUP-A OF GROUP-B RBC

UNIT/PART IV	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 26-3-18	DISCRETE MESSAGES	GROUP-B OF
DAY2 DATE 27-3-18	THE CONCEPT OF AMOUNT OF INFORMATION	GROUP-A JFET GROUP-B JFET
DAY3 DATE 28-3-18	AVERAGE INFORMATION	GROUP-A OF GROUP-B DAC
DAY4 DATE 2-4-18	ENTROPY	GROUP-B REVISION-AM
DAY5 DATE 3-4-18	INFORMATION RATE	GROUP-A REVISION-CRO GROUP-B REVISION-CRO
DAY6 DATE 4-4-18	SHANON- FANO CODING	GROUP-A REVISION-AM GROUP-B SIPO SR
DAY7 DATE 9-4-18	SHANON'S THEOREM	GROUP-B REVISION-FM
DAY8 DATE 10-4-18	CHANNEL CAPACITY	GROUP-A REVISION-PNPN GROUP-B REVISION- PNPN
DAY9 DATE 11-4-18	COMPARISON OF ERROR RATE IN CODES AND ENCODED TRANSMISSION	GROUP-A REVISION-FM GROUP-B SIPO SR
DAY10 DATE 16-4-18	HUFFMAN CODING	GROUP-B REVISION-FM
DAY11 DATE 17-4-18	SHANON THEOREM AND ITS IMPLICATIONS	GROUP-A REVISION-PNPN GROUP-B REVISION- PNPN
DAY12 DATE 18-4-18	HOLIDAY	

NAME OF ASSISTANT /ASSOCIATE PROFESSOR: GURMEET SINGH
CLASS/SECTION : B.Sc (H) I.T-IVth SEM
SUBJECT :MICROPROCESSOR ARCHITECTURE AND PROGRAMMING-II

UNIT/PART I	TOPIC	
	THEORY(BSIT-404)	PRACTICAL(BSIT-408)
DAY1 DATE 4-1-18	INTERFACING AN 8-BIT DAC WITH 8085	GROUP-A MULTIPLICATION/DIVISION
DAY2 DATE 5-1-18	INTERFACING AN 8-BIT DAC WITH 8085	GROUP-B MULTIPLICATION/DIVISION
DAY3 DATE 6-1-18	INTERFACING AN 8-BIT ADC WITH 8085	GROUP-C MULTIPLICATION/DIVISION
DAY4 DATE 11-1-18	GENERAL PURPOSE PROGRAMMABLE PERIPHERAL DEVICES	GROUP-A MULTIPLICATION/DIVISION
DAY5 DATE 12-1-18	BLOCK DIAGRAM OF 8255	GROUP-B MULTIPLICATION/DIVISION

DAY6 DATE 13-1-18	MODES OF OPERATION OF 8255-MODE0	GROUP-C MULTIPLICATION/DIVISION
DAY7 DATE 18-1-18	BSR MODE	GROUP-A SMALLEST/LARGEST NUMBER
DAY8 DATE 19-1-18	MODE1	GROUP-B SMALLEST/LARGEST NUMBER
DAY9 DATE 20-1-18	MODE1	GROUP-C SMALLEST/LARGEST NUMBER
DAY10 DATE 25-1-18	MODE2	GROUP-A SMALLEST/LARGEST NUMBER
DAY11 DATE 26-1-18	HOLIDAY	
DAY 12 DATE 27-1-18	MODE2	GROUP-C SMALLEST/LARGEST NUMBER
UNIT/PART II	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 1-2-18	8254 (8253) PROGRAMMABLE INTERVAL TIMER	GROUP-A ASCENDING/DECENDING ORDER
DAY2 DATE 2-2-18	BLOCK DIAGRAM OF 8254	GROUP-B SMALLEST/LARGEST NUMBER
DAY3 DATE 3-2-18	MODES OF OPERATION OF 8254-MODE0	GROUP-C ASCENDING/DECENDING ORDER
DAY4 DATE 8-2-18	MODE1, MODE2	GROUP-A ASCENDING/DECENDING ORDER
DAY5 DATE 9-2-18	MODE3, ASSIGNMENT 1	GROUP-B ASCENDING/DECENDING ORDER
DAY6 DATE 10-2-18	HOLIDAY	
DAY7 DATE 15-2-18	MODE4, MODE5	GROUP-A EVEN PARITY/ODD PARITY
DAY8 DATE 16-2-18	8237 DMA CONTROLLER	GROUP-B ASCENDING/DECENDING ORDER
DAY9 DATE 17-2-18	8237 DMA CONTROLLER	GROUP-C ASCENDING/DECENDING ORDER

DAY10 DATE 22-2-18	BLOCK DIAGRAM OF 8279 PROGRAMMABLE KEYBOARD/DISPLAY INTERFACE	GROUP-A GENERATE SQUARE WAVE
DAY11 DATE 23-2-18	BLOCK DIAGRAM OF 8279 PROGRAMMABLE KEYBOARD/DISPLAY INTERFACE	GROUP-B EVEN PARITY/ODD PARITY
UNIT/PART III	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 24-2-18	MICROPROCESSOR EVOLUTION AND TYPES	GROUP-C EVEN PARITY/ODD PARITY
DAY2 DATE 1-3-18	HOLIDAY	
DAY3 DATE 2-3-18	HOLIDAY	
DAY4 DATE 3-3-18	HOLIDAY	
DAY5 DATE 8-3-18	8086 MICROPROCESSOR FAMILY OVERVIEW	GROUP-A BINARY TO GRAY CONVERSION
DAY6 DATE 9-3-18	8086 INTERNAL ARCHITECTURE	GROUP-B GENERATE SQUARE WAVE
DAY7 DATE 10-3-18	8086 INTERNAL ARCHITECTURE ASSIGNMENT 2	GROUP-C GENERATE SQUARE WAVE
DAY8 DATE 15-3-18	CONDITIONAL TEST	GROUP-A SOLVE THE LOGICAL EQUATION
DAY9 DATE 16-3-18	CONDITIONAL TEST	GROUP-B BINARY TO GRAY CONVERSION
DAY10 DATE 17-3-18	INTRODUCTION TO PROGRAMMING THE 8086, PROGRAMMING DEVELOPMENT STEPS	GROUP-C BINARY TO GRAY CONVERSION
DAY11 DATE 22-3-18	CONSTRUCTING THE MACHINE CODES FOR 8086 INSTRUCTIONS	GROUP-A GENERATE TIME DELAY
DAY 12 DATE 23-3-18	HOLIDAY	
DAY 13 DATE 24-3-18	CONSTRUCTING THE MACHINE CODES FOR 8086 INSTRUCTIONS, WRITING PROGRAM FOR USE WITH AN ASSEMBLER	GROUP-C SOLVE THE LOGICAL EQUATION

UNIT/PART IV	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 29-3-18	HOLIDAY	
DAY2 DATE 30-3-18	ASSEMBLY LANGUAGE PROGRAM DEVELOPMENT TOOLS	GROUP-B SOLVE THE LOGICAL EQUATION
DAY3 DATE 31-3-18	SIMPLE SEQUENCE PROGRAM, JUMPS, FLAGS AND CONDITIONAL JUMPS	GROUP-C GENERATE TIME DELAY
DAY4 DATE 5-4-18	IF-THEN PROGRAM	GROUP-A REVISION OF 8085 KIT AND PROGRAMMING
DAY5 DATE 6-4-18	IF-THEN- ELSE PROGRAM	GROUP-B GENERATE TIME DELAY
DAY6 DATE 7-4-18	MULTIPLE IF-THEN- ELSE PROGRAM	GROUP-C REVISION OF 8085 KIT AND PROGRAMMING
DAY7 DATE 12-4-18	WHILE- DO PROGRAMS	GROUP-A REVISION OF 8085 KIT AND PROGRAMMING
DAY8 DATE 13-4-18	WHILE- DO PROGRAMS	GROUP-B REVISION OF 8085 KIT AND PROGRAMMING
DAY9 DATE 14-4-18	HOLIDAY	
DAY10 DATE 19-4-18	REPEAT –UNTIL PROGRAMS	GROUP-A REVISION OF 8085 KIT AND PROGRAMMING
DAY11 DATE 20-4-18	INSTRUCTION TIMING AND DELAY LOOPS	GROUP-B REVISION OF 8085 KIT AND PROGRAMMING

NAME OF TEACHER:-GURMEET SINGH