

LESSON PLAN FOR EVEN SEM
SESSION 2017-18

NAME OF ASSISTANT PROFESSOR : SUSHMA
CLASS/SECTION : B.SC (ELECT. VITH SEM)/D
SUBJECT : INTRODUCTION TO C & IT'S PROGRAMMING

UNIT/PART I	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 1-1-18	C FUNDAMENTALS
DAY2 DATE 2-1-18	THE CHARACTER SET, IDENTIFIER & KEYWORDS
DAY3 DATE 3-1-18	DATA TYPE, CONSTANTS
DAY4 DATE 8-1-18	VARIABLE & ARRAYS DECLARATION
DAY5 DATE 9-1-18	EXPRESSION STATEMENTS
DAY6 DATE 10-1-18	OPERATORS
DAY7 DATE 15-1-18	EXPRESSIONS: ARITHMETIC OPERATORS, UNARY OPERATORS
DAY 8 DATE 16-1-18	RELATIONAL & LOGICAL OPERATORS
DAY9 DATE 17-1-18	ASSIGNMENT & CONDITIONAL OPERATORS
DAY10 DATE 22-1-18	HOLIDAY	
DAY11 DATE 23-1-18	SPORTS DAY	
DAY12 DATE 24-1-18	HOLIDAY	
UNIT/PART II	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 29-1-18	DATA INPUT AND OUTPUT
DAY2 DATE 30-1-18	ENTERING INPUT DATA-THE SCANNED FUNCTION
DAY3 DATE 31-1-18	HOLIDAY	
DAY4 DATE 5-2-18	WRITING OUTPUT DATA- THE PRINTF FUNCTION
DAY5	CONTROL STATEMENT: WHILE

DATE 6-2-18	STATEMENT	
DAY6 DATE 7-2-18	DO-WHILE STATEMENT
DAY7 DATE 12-2-18	ASSIGNMENT, FOR STATEMENT
DAY8 DATE 13-2-18	HOLIDAY	
DAY9 DATE 14-2-18	IF-ELSE STATEMENT
DAY10 DATE 19-2-18	SWITCH STATEMENT
DAY11 DATE 20-2-18	CONTINUE STATEMENT
DAY12 DATE 21-2-18	NUMERICAL PROBLEMS BASED ON UNIT
UNIT/PART III	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 26-2-18	FUNCTION
DAY2 DATE 27-2-18	DEFINING A FUNCTION
DAY3 DATE 28-2-18	HOLIDAY	
DAY4 DATE 5-3-18	ACCESSING A FUNCTION
DAY5 DATE 6-3-18	ACCESSING A FUNCTION CONTINUE...
DAY6 DATE 7-3-18	PASSING ARGUMENTS TO A FUCTION
DAY 7 DATE 12-3-18	SPECIFY ARGUMENTS
DAY8 DATE 13-3-18	SPECIFY ARGUMENTS CONTINUE...
DAY9 DATE 14-3-18	DATA TYPE
DAY10 DATE 19-3-18	PROGRAMMING BASED ON UNIT
DAY11 DATE 20-3-18	REVISION OF UNIT
DAY12 DATE 21-3-18	CONDITIONAL TEST
UNIT/PART IV	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 26-3-18	ARRAY

DAY2 DATE 27-3-18	DEFINING A ARRAY PROCESSING AN ARRAY
DAY3 DATE 28-3-18	PASSING ARRAY TO A FUNCTION
DAY4 DATE 2-4-18	MULTIDIMENSIONAL ARRAYS
DAY5 DATE 3-4-18	ARRAY AND STRINGS
DAY6 DATE 4-4-18	ARRAY AND STRINGS CONTINUE...
DAY7 DATE 9-4-18	POINTERS: FUNDAMENTALS
DAY8 DATE 10-4-18	POINTERS DECLARATION
DAY9 DATE 11-4-18	PASSING POINTER TO A FUNCTION
DAY10 DATE 16-4-18	PASSING POINTER TO A FUNCTION CONTINUE...
DAY11 DATE 17-4-18	POINTERS AND ONE DIMENSIONAL ARRAY, OPERATIONS ON POINTERS
DAY12 DATE 18-4-18	HOLIDAY	

NAME OF ASSISTANT PROFESSOR: SUSHMA

CLASS/SECTION: B.SC (ELECT. IVTH SEM)/D

SUBJECT : OP-AMP AND LINEAR INTEGRATED CIRCUITS-II

UNIT/PART I	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 1-1-18	FEEDBACK:CLASSIFICATION OF AMPLIFIERS	GROUP-1: IC-555 AS AMV
DAY2 DATE 2-1-18	FEEDBACK CONCEPT AND CALCULATION OF TRANSFER GAIN	GROUP-1: IC-555 AS AMV
DAY3 DATE 3-1-18	FEEDBACK TOPOLOGIES	GROUP-2: OP AMP AS DIFFERENTIATOR
DAY4 DATE 5-1-18	GROUP-3: J-K FLIP-FLOP
DAY5 DATE 6-1-18	GROUP-3:J-K FLIP-FLOP
DAY6 DATE 8-1-18	EFFECT OF NEGATIVE FEEDBACK ON GAIN	GROUP-1: IC-555 AS AMV

DAY7 DATE 9-1-18	NON LINEAR DISTORTION	GROUP-1: IC-555 AS AMV
DAY8 DATE 10-1-18	FREQUENCY RESPONSE	GROUP-2: OP AMP AS DIFFERENTIATOR
DAY9 DATE 12-1-18	GROUP-3: T FLIP-FLOP
DAY10 DATE 13-1-18	GROUP-3: T FLIP-FLOP
DAY11 DATE 15-1-18	INPUT RESISTANCE IN TOPOLOGIES	GROUP-1: IC-555 AS MMV
DAY 12 DATE 16-1-18	OUTPUT RESISTANCE IN VARIOUS TOPOLOGIES	GROUP-1: IC-555 AS MMV
DAY13 DATE 17-1-18	OUTPUT RESISTANCE IN VARIOUS TOPOLOGIES CONTINUE...	GROUP-2: OP AMP AS INTEGRATOR
DAY14 DATE 19-1-18	GROUP-3: D FLIP-FLOP
DAY15 DATE 20-1-18	GROUP-3: D FLIP-FLOP
DAY16 DATE 22-1-18	HOLIDAY	
DAY17 DATE 23-1-18	SPORTS DAY	
DAY18 DATE 24-1-18	HOLIDAY	
DAY19 DATE 26-1-18	HOLIDAY	
DAY 20 DATE 27-1-18	GROUP-3: RIPPLE BINARY COUNTER
UNIT/PART II	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 29-1-18	SINUSOIDAL OSCILLATORS	GROUP-1: IC-555 AS MMV
DAY2 DATE 30-1-18	PRINCIPLE OF OSCILLATIONS	GROUP-1: IC-555 AS MMV
DAY3 DATE 31-1-18	CONDITION OF SUSTAIN OSCILLATION	GROUP-2: OP AMP AS INTEGRATOR
DAY5 DATE 2-2-18	GROUP-3: RIPPLE BINARY COUNTER
DAY6 DATE 3-2-18	GROUP-3: RIPPLE BINARY COUNTER
DAY7 DATE 5-2-18	RF OSCILLATORS	GROUP-1: RIPPLE BINARY COUNTER
DAY8 DATE 6-2-18	HARTLEY OSCILLATOR	GROUP-1: RIPPLE BINARY COUNTER
DAY9	COLPITTS OSCILLATORS	GROUP-2: IC-555 AS AMV

DATE 7-2-18		
DAY11 DATE 9-2-18	GROUP-3: RIPPLE BINARYCOUNTER
DAY12 DATE 10-2-18	HOLIDAY	
DAY13 DATE 12-2-18	ASSIGNMENT, COLPITTS OSCILLATORS (CONTINUE...)	GROUP-1: RIPPLE BINARY COUNTER
DAY14 DATE 13-2-18	HOLIDAY	
DAY15 DATE 14-2-18	CRYSTAL OSCILLATOR	GROUP-2: IC-555 AS AMV
DAY17 DATE 16-2-18	GROUP-3: SHIFT REGISTER SISO
DAY18 DATE 17-2-18	GROUP-3: SHIFT REGISTER SISO
DAY19 DATE 19-2-18	AF OSCILLATORS	GROUP-1: RIPPLE BINARY COUNTER
DAY20 DATE 20-2-18	WEIN BRIDGE OSCILLATOR	GROUP-1: RIPPLE BINARY COUNTER
DAY21 DATE 21-2-18	PHASE SHIFT OSCILLATOR	GROUP-2: IC-555 AS AMV
DAY23 DATE 23-2-18	GROUP-3: SHIFT REGISTER SIPO
UNIT/PART III	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 24-2-18	GROUP-3: SHIFT REGISTER SIPO
DAY2 DATE 26-2-18	BASIC IDEA OF MULTIVIBRATOR	GROUP-1: SHIFT REGISTER SISO
DAY3 DATE 27-2-18	ASTABLE MULTIVIBRATOR USING BJT	GROUP-1: SHIFT REGISTER SISO
DAY4 DATE 28-2-18	BISTABLE MULTIVIBRATOR USING BJT	GROUP-2: IC-555 AS MMV
DAY5 DATE 2-3-18	HOLIDAY	
DAY6 DATE 3-3-18	HOLIDAY	
DAY7 DATE 5-3-18	MONOSTABLE MULTIVIBRATOR USING BJT	GROUP-1: SHIFT REGISTER SIPO
DAY8 DATE 6-3-18	SILICON CONTROLLED RECTIFIER	GROUP-1: SHIFT REGISTER SIPO
DAY9 DATE 7-3-18	TRIAC	GROUP-2: IC-555 AS MMV
DAY10 DATE 9-3-18	GROUP-3: D TO A CONVERTER

DAY11 DATE 10-3-18	GROUP-3: D TO A CONVERTER
DAY 12 DATE 12-3-18	DIAC	GROUP-1: SHIFT REGISTER SIPO
DAY13 DATE 13-3-18	TRIANGULAR WAVEFORM GENERATOR	GROUP-1: SHIFT REGISTER SIPO
DAY14 DATE 14-3-18	THE 555 TIMER	GROUP-2: CALCULATION OF H-PARAMETER
DAY15 DATE 16-3-18	GROUP-3: D TO A CONVERTER
DAY16 DATE 17-3-18	BLOCK DIAGRAM OF 555	GROUP-3: CALCULATION OF H- PARAMETER
DAY17 DATE 19-3-18	555 AS A ASTABLE MULTIVIBRATOR	GROUP-1: CALCULATION OF H- PARAMETER
DAY18 DATE 20-3-18	555 AS A MONOSTABLE MULTIVIBRATOR	GROUP-1: CALCULATION OF H- PARAMETER
DAY19 DATE 21-3-18	555 AS A MONOSTABLE MULTIVIBRATOR(CONTINUE)	GROUP-2: CALCULATION OF H- PARAMETER
DAY 20 DATE 23-3-18	HOLIDAY	
DAY 21 DATE 24-3-18	GROUP-3: AMV USING TRANSISTOR
UNIT/PART IV	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 26-3-18	CONDITIONAL TEST	GROUP-1: CALCULATION OF H- PARAMETER
DAY2 DATE 27-3-18	BASIC IDEA OF REGULATED POWER SUPPLY	GROUP-1: CALCULATION OF H- PARAMETER
DAY3 DATE 28-3-18	PRINCIPLE OF ELECTRONIC VOLTAGE REGULATORS	GROUP-2: CALCULATION OF H- PARAMETER
DAY4 DATE 30-3-18	GROUP-3: AMV USING TRANSISTOR
DAY5 DATE 31-3-18	GROUP-3: AMV USING TRANSISTOR
DAY6 DATE 2-4-18	ZENER DIODE SHUNT REGULATOR	GROUP-1: D TO A CONVERTER
DAY7 DATE 3-4-18	BJT SHUNT REGULATOR	GROUP-1: D TO A CONVERTER
DAY8 DATE 4-4-18	BJT SERIES REGULATOR	GROUP-2: SEMINAR BASED ON EXPERIMENTS
DAY9 DATE 6-4-18	GROUP-3: MMV USING TRANSISTOR
DAY10 DATE 7-4-18	GROUP-3: MMV USING TRANSISTOR
DAY11	POWER SUPPLY	GROUP-1: D TO A

DATE 9-4-18		CONVERTER
DAY12 DATE 10-4-18	REGULATION USING OP AMP	GROUP-1: D TO A CONVERTER
DAY13 DATE 11-4-18	LOAD REGULATION	GROUP-2: SEMINAR BASED ON EXPERIMENTS
DAY14 DATE 13-4-18	GROUP-3: MMV USING TRANSISTOR
DAY15 DATE 14-4-18	HOLIDAY	GROUP-3: SEMINAR BASED ON EXPERIMENTS
DAY16 DATE 16-4-18	SHORT CIRCUIT PROTECTION	GROUP-1: SEMINAR BASED ON EXPERIMENTS
DAY17 DATE 17-4-18	CURRENT REGULATION USING OP AMP	GROUP-1: SEMINAR BASED ON EXPERIMENTS
DAY18 DATE 18-4-18	HOLIDAY	
DAY19 DATE 20-4-18	GROUP-3: SEMINAR BASED ON EXPERIMENTS

NAME OF ASSISTANT PROFESSOR: SUSHMA
CLASS/SECTION: B.SC (ELECT. IIND SEM)/D
SUBJECT : ELECTRONIC DEVICES AND CIRCUIT-I

UNIT/PART I	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 4-1-18	UNIT-1 DISCUSSION ABOUT BIASING OF TRANSISTOR
DAY2 DATE 5-1-18	WHY BIAS A TRANSISTOR	GROUP-F2: CRO
DAY3 DATE 6-1-18	SELECTION OF OPERATING POINT	GROUP-F2: CRO
DAY4 DATE 11-1-18	NEED FOR BIAS STABILIZATION
DAY5 DATE 12-1-18	REQUIREMENT OF A BIASING CIRCUIT	GROUP-F2: CRO
DAY6 DATE 13-1-18	DIFFERENT TYPES OF BIASING CIRCUIT	GROUP-F2:CRO
DAY7 DATE 18-1-18	FIXED BIAS CIRCUIT
DAY8 DATE 19-1-18	FIXED BIAS CIRCUIT (CONTINUE...)	GROUP-F2: DTL NAND GATE
DAY9 DATE 20-1-18	COLLECTOR TO BASE BIAS CIRCUIT	GROUP-F2:DTL NAND GATE
DAY10	HOLIDAY

DATE 22-1-18		
DAY11 DATE 25-1-18	COLLECTOR TO BASE BIAS CIRCUIT (CONTINUE...)
DAY12 DATE 26-1-18	HOLIDAY
DAY 13 DATE 27-1-18	NUMERICAL PROBLEMS BASED ON BIASING OF A TRANSISTOR	GROUP-F2: DTL NAND GATE
UNIT/PART II	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 1-2-18	BIAS CIRCUIT WITH EMITTER RESISTOR
DAY2 DATE 2-2-18	BIAS CIRCUIT WITH EMITTER RESISTOR (CONTINUE...)	GROUP-F2: TTL NAND GATE
DAY3 DATE 3-2-18	VOLTAGE DIVIDER BIASING CIRCUIT	GROUP-F2: TTL NAND GATE
DAY4 DATE 8-2-18	VOLTAGE DIVIDER BIASING CIRCUIT (CONTINUE...)
DAY5 DATE 9-2-18	ASSIGNMENT, NUMERICAL PROBLEMS	GROUP-F2: CB CH. & CALCULATION H PARAMETERS
DAY6 DATE 10-2-18	HOLIDAY
DAY7 DATE 15-2-18	EMITTER BIAS CIRCUIT
DAY8 DATE 16-2-18	EMITTER BIAS CIRCUIT (CONTINUE...)	GROUP-F2: CB CH. & CALCULATION H PARAMETERS
DAY9 DATE 17-2-18	GAIN OF A MULTISTAGE AMPLIFIER	GROUP-F2: CB CH. & CALCULATION H PARAMETERS
DAY10 DATE 22-2-18	GAIN OF A MULTISTAGE AMPLIFIER (CONTINUE...)
DAY11 DATE 23-2-18	NUMERICAL PROBLEMS BASED ON BIASING OF A TRANSISTOR	GROUP-F2: CE CH. & CALCULATION H PARAMETERS
UNIT/PART III	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 24-2-18	INTRODUCTION OF COUPLING OF TRANSISTOR	GROUP-F2: CE CH. & CALCULATION H PARAMETERS

DAY2 DATE 1-3-18	HOLIDAY	
DAY3 DATE 2-3-18	HOLIDAY	
DAY4 DATE 3-3-18	HOLIDAY	
DAY5 DATE 8-3-18	HOW TO COUPLE TWO STAGES
DAY6 DATE 9-3-18	RESISTANCE-CAPACITANCE COUPLING	GROUP-F2:CB CH. & CALCULATION H PARAMETERS
DAY7 DATE 10-3-18	TRANSFORMER COUPLING	GROUP-F2:CB CH. & CALCULATION H PARAMETERS
DAY8 DATE 15-3-18	FREQUENCY RESPONSE CURVE OF AN RC COUPED AMPLIFIER
DAY9 DATE 16-3-18	FALL OF GAIN IN LOW FREQUENCY RANGE	GROUP-F2: JFET
DAY10 DATE 17-3-18	GAIN FALL IN HIGH FREQUENCY	GROUP-F2: JFET
DAY11 DATE 22-3-18	CONDITIONAL TEST
DAY 12 DATE 23-3-18	HOLIDAY	
DAY 13 DATE 24-3-18	BANDWIDTH OF AN AMPLIFIER	GROUP-F2: JFET
UNIT/PART IV	TOPIC	
	THEORY	PRACTICAL
DAY1 DATE 29-3-18	HOLIDAY	
DAY2 DATE 30-3-18	DISCUSSION ABOUT JFET	GROUP-F2:ZENER DIODE AS Z VOLTAGE REGULATOR
DAY3 DATE 31-3-18	QUALITATIVE DESCRIPTION OF JFET	GROUP-F2: ZENER DIODE AS Z VOLTAGE REGULATOR
DAY4 DATE 5-4-18	DRAIN CHARACTERISTICS OF JFET
DAY5 DATE 6-4-18	TRANSFER CHARACTERISTICS OF JFET	GROUP-F2: ZENER DIODE AS Z VOLTAGE REGULATOR
DAY6 DATE 7-4-18	LOW FREQUENCY MODEL OF FET	GROUP-F2: ZENER DIODE AS Z VOLTAGE REGULATOR

DAY7 DATE 12-4-18	CS MODEL LOW FREQUENCY
DAY8 DATE 13-4-18	CD MODEL LOW FREQUENCY	GROUP-F2: SEMINAR BASED ON EXPERIMENTS
DAY9 DATE 14-4-18	HOLIDAY	
DAY10 DATE 18-4-18	HOLIDAY	
DAY11 DATE 19-4-18	NUMERICAL PROBLEMS BASED ON JFET
DAY12 DATE 20-4-18	REVIEW OF UNIT-1,2,3AND 4	GROUP-F2: SEMINAR BASED ON EXPERIMENTS